

FIG. 1
Prior Art

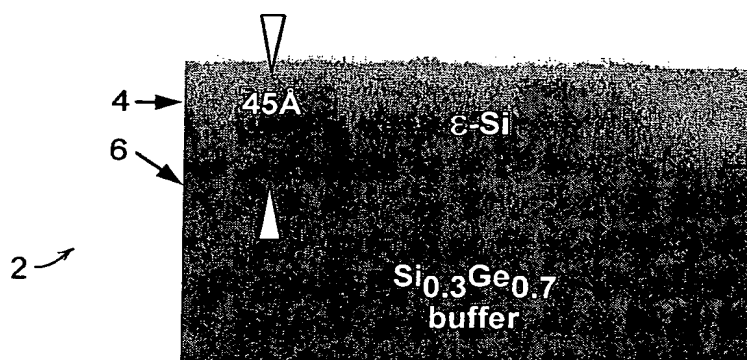


FIG. 2A

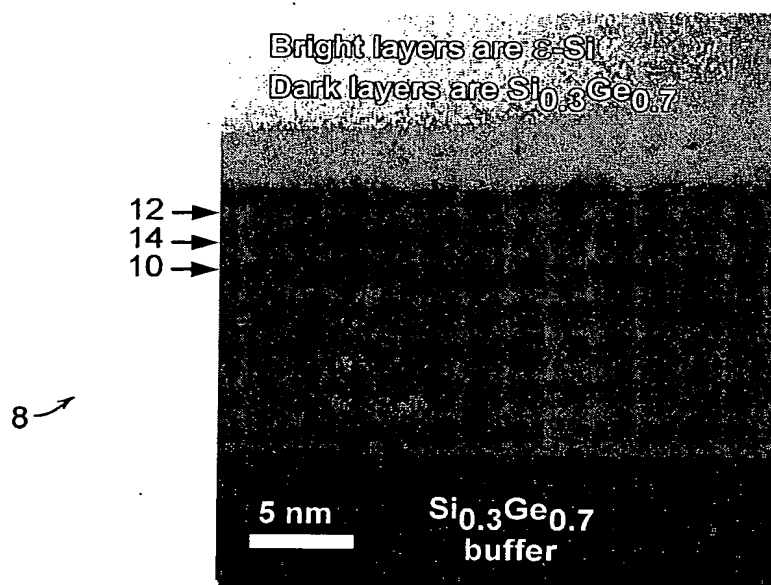


FIG. 3A

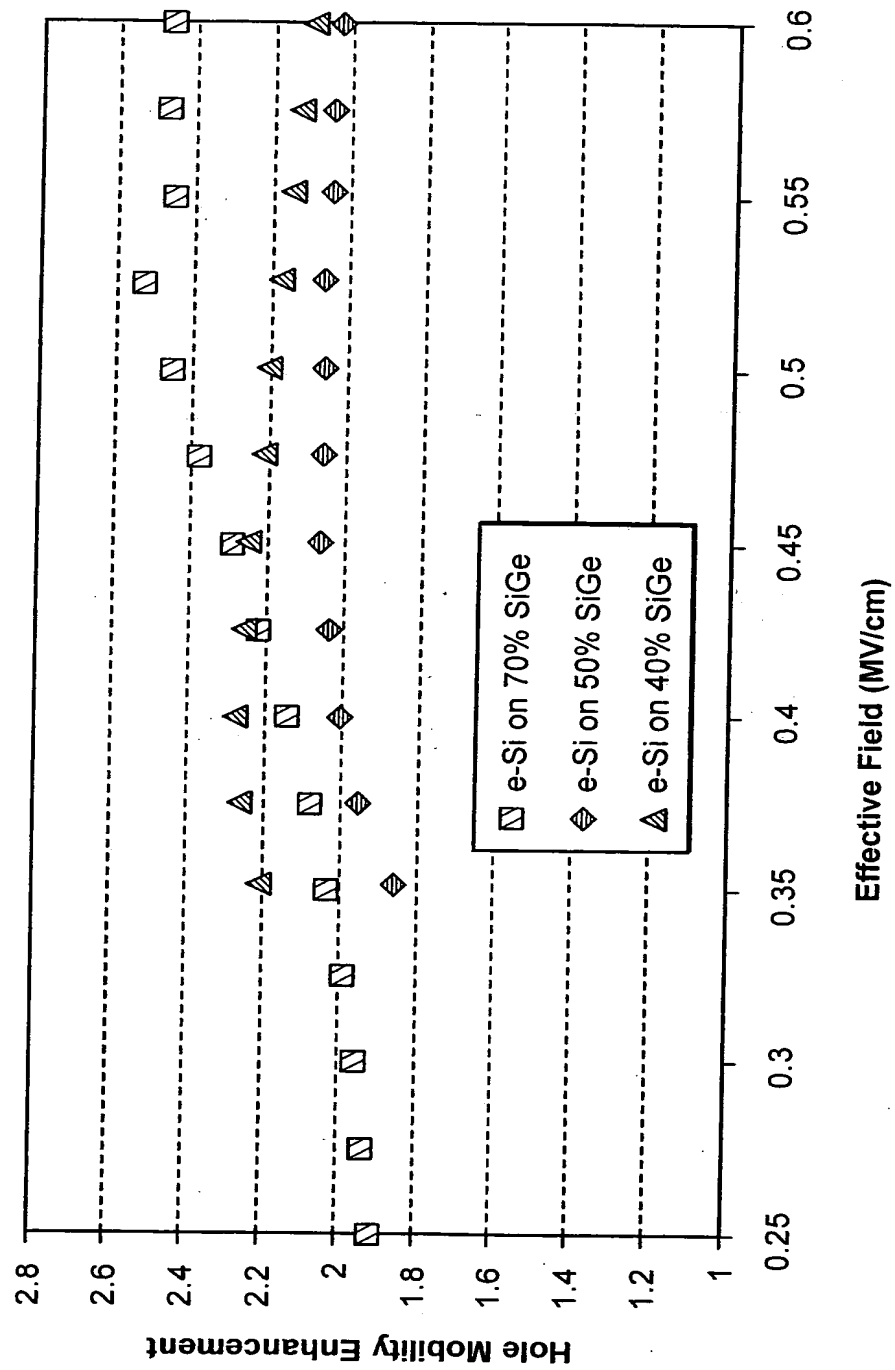
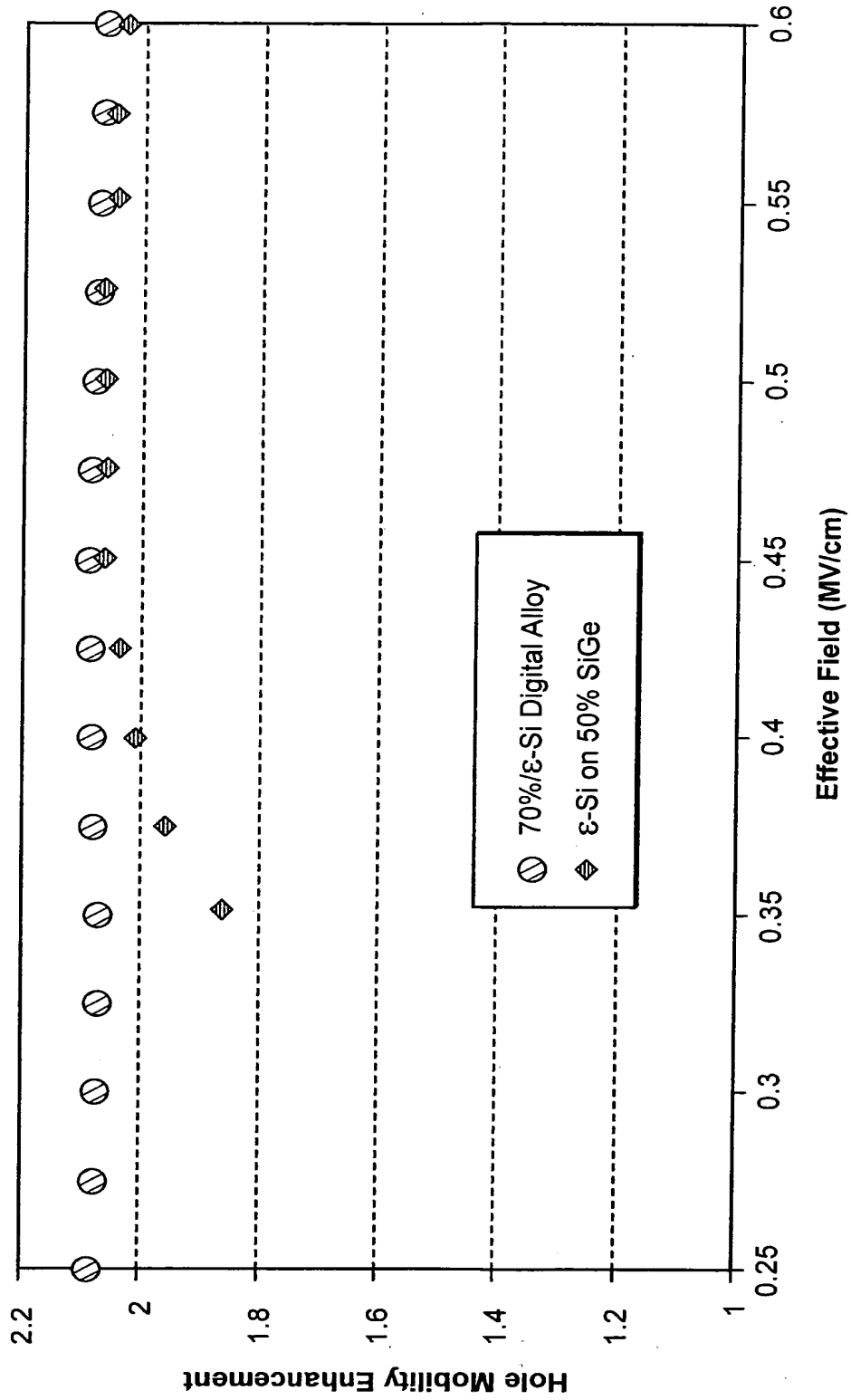


FIG. 2B



Mobility enhancement of ϵ -Si/Si_{0.3}Ge_{0.7} digital alloy pMOSFET compared to conventional ϵ -Si device

FIG. 3B